

# **1.2V Drive Nch MOSFET**

## RUU002N05

## Structure

Silicon N-channel MOSFET

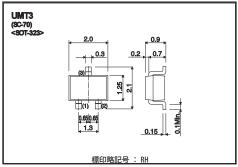
## Features

- 1) High speed switing.
- 2) Small package(UMT3).
  3) Ultra low voltage drive(1.2V drive).

## Application

Switching

#### Dimensions (Unit : mm)



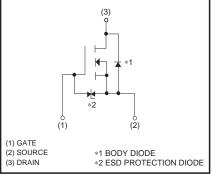
## Packaging specifications

	Package	Taping
Туре	Code	T106
	Basic ordering unit (pieces)	3000
RUU002N0	05	0



Unit V V mΑ mΑ mΑ mΑ mW °C

°C



Param	neter	Symbol	Limits
Drain-source voltage		V <sub>DSS</sub>	50
Gate-source voltage		V <sub>GSS</sub>	±8
Drain current	Continuous	I <sub>D</sub>	±200
	Pulsed	<mark>ا</mark> 1 ا	±800
Source current	Continuous		150

Drain current		D	
	Pulsed	<mark>ا</mark> 1 ا	±800
Source current	Continuous	I <sub>S</sub>	150
(Body Diode)	Pulsed	ا <sub>sP</sub> 1	800
Power dissipation		P <sub>D</sub> *2	200
Channel temperature		Tch	150

\*1 Pw≤10µs, Duty cycle≤1%

Range of storage temperature

\*2 Each terminal mounted on a recommended land.

•Absolute maximum ratings (Ta = 25°C)

#### Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	625	°C / W

Tsta

\* Each terminal mounted on a recommended land

-55 to +150

## •Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	50	-	-	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	0.3	-	1.0	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
		-	1.6	2.2		I <sub>D</sub> =200mA, V <sub>GS</sub> =4.5\
Ctatia drain aguras en stata		-	1.7	2.4		I <sub>D</sub> =200mA, V <sub>GS</sub> =2.5\
Static drain-source on-state resistance	R <sub>DS (on)</sub>	-	1.9	2.7	Ω	I <sub>D</sub> =100mA, V <sub>GS</sub> =1.8\
resistance		-	2.0	4.0		I <sub>D</sub> =40mA, V <sub>GS</sub> =1.5V
		-	2.4	7.2		I <sub>D</sub> =20mA, V <sub>GS</sub> =1.2V
Forward transfer admittance	۱ Y <sub>fs</sub> ľ	0.4	-	-	S	I <sub>D</sub> =200mA, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	-	25	-	pF	V <sub>DS</sub> =10V
Output capacitance	C <sub>oss</sub>	-	6	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	-	3	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	-	4	-	ns	I <sub>D</sub> =100mA, V <sub>DD</sub> ≒ 30∖
Rise time	t <sub>r</sub> *	-	6	-	ns	V <sub>GS</sub> =4.5V
Turn-off delay time	t <sub>d(off)</sub> *	-	15	-	ns	R <sub>L</sub> =300Ω
Fall time	t <sub>f</sub> *	-	55	-	ns	R <sub>G</sub> =10Ω

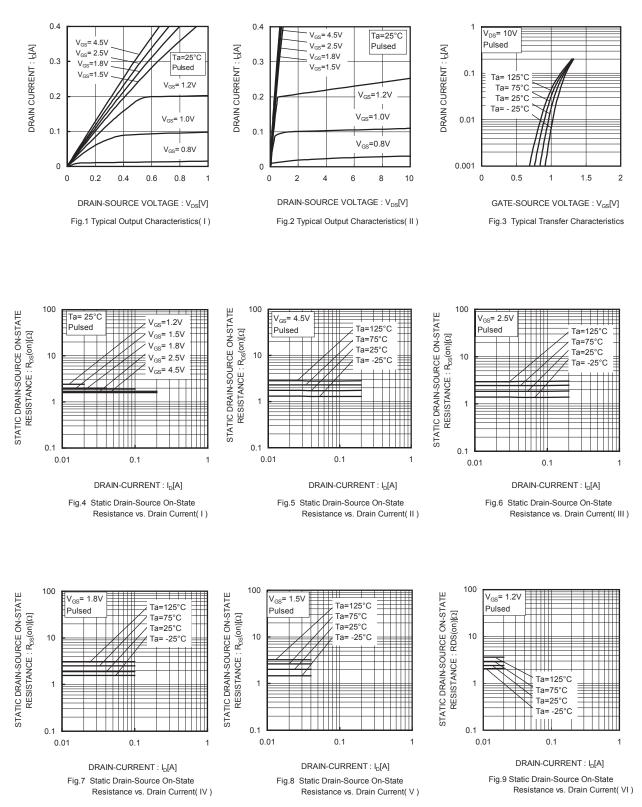
\*Pulsed

## •Body diode characteristics (Source-Drain) (Ta = 25°C)

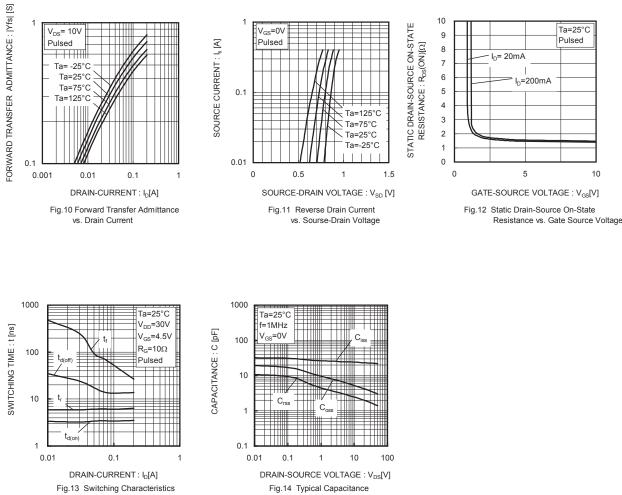
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	$V_{SD}^{*}$	-	-	1.2	V	I <sub>s</sub> =200mA, V <sub>GS</sub> =0V

\*Pulsed

### •Electrical characteristic curves



## RUU002N05



vs. Drain-Source Voltage

## Measurement circuits

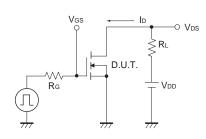


Fig.1-1 Switching time measurement circuit

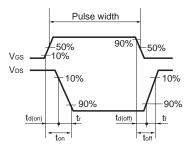


Fig.1-2 Switching waveforms

#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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